Hex Schmitt-trigger Inverters

HITACHI

ADE-205-064B(Z) Rev.2 September 1995

Description

The HD74LVC14 has six schmitt trigger inverters in a 14 pin package. Low voltage and high speed operation is suitable at the battery drive product (note type personal computer) and low power consumption extends the life of a battery for long time operation.

Features

- $V_{CC} = 2.0 \text{ V to } 5.5 \text{ V}$
- All inputs V_{IH} (Max.) = 5.5 V (@ V_{CC} = 0 V to 5.5 V)
- Typical V_{OL} ground bounce < 0.8 V (@ V_{CC} = 3.3 V, Ta = 25°C)
- Typical V_{OH} undershoot > 2.0 V (@ V_{CC} = 3.3 V, Ta = 25°C)
- High output current ± 24 mA (@V_{CC} = 3.0 V to 5.5 V)

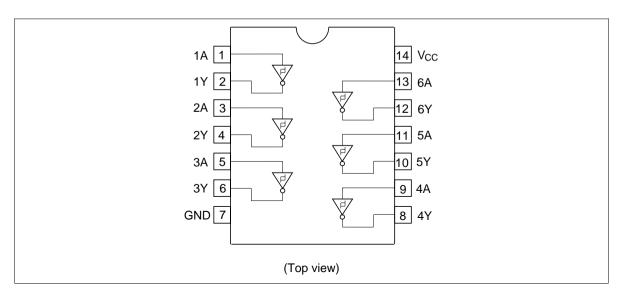
Function Table

Input A	Output Y
L	Н
Н	L

H: High level
L: Low level



Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Conditions
Supply voltage range	V _{cc}	-0.5 to 6.0	V	
Input diode current	I _{IK}	-50	mA	V ₁ = -0.5 V
Input voltage	Vı	-0.5 to 6.0	V	
Output diode current	I _{ok}	- 50	mA	V ₀ = -0.5 V
		50	mA	V _o = V _{cc} +0.5 V
Output voltage	V _o	-0.5 to V _{cc} +0.5	V	
Output current	Io	±50	mA	
V _{cc} , GND current / pin	I _{CC} or I _{GND}	100	mA	
Storage temperature	Tstg	-65 to +150	°C	

Note: The absolute maximum ratings are values which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V _{cc}	1.5 to 5.5	V	Data retention
		2.0 to 5.5	V	At operation
Input / Output voltage	Vı	0 to 5.5	V	A
	V_{o}	0 to V _{cc}	V	Υ
Operating temperature	Та	-40 to 85	°C	
Output current	I _{OH}	-12	mA	$V_{CC} = 2.7 \text{ V}$
		-24 ^{*2}	mA	$V_{cc} = 3.0 \text{ V to } 5.5 \text{ V}$
	I _{OL}	12	mA	$V_{cc} = 2.7 \text{ V}$
		24*2	mA	$V_{cc} = 3.0 \text{ V to } 5.5 \text{ V}$
Input rise / fall time*1	t _r , t _f	10	ns/V	

Notes: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

2. duty cycle ≤ 50%

Electrical Characteristics

Ta	= -40	to	85°C
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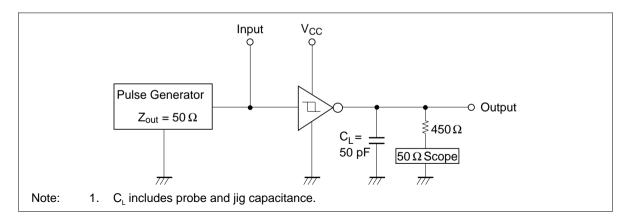
Item	Symbol	V _{cc} (V)	Min	Max	Unit	Test Conditions	
Threshould voltage	V_T^+	2.7	1.0	2.0	V		
		3.0	1.2	2.2	V	-	
		3.6	1.5	2.4	V	-	
		4.5	1.6	2.6	V	-	
		5.5	2.0	3.0	V	=	
	V _T	2.7	0.4	1.4	V	-	
		3.0	0.6	1.5	V	=	
		3.6	0.8	1.8	V	=	
		4.5	1.0	2.0	V	=	
		5.5	1.4	2.4	V	=	
Hysteresis voltage	V_{H}	2.7	0.3	1.1	V	$V_T^+ - V_T^-$	
		3.0	0.4	1.2	V	=	
		3.6	0.4	1.2	V	=	
		4.5	0.4	1.2	V	=	
		5.5	0.4	1.2	V	=	
Input voltage	V _{IH}	2.7 to 3.6	2.4	_	V		
		4.5 to 5.5	3.0	_	V	=	
	V _{IL}	2.7 to 3.6	_	0.4	V	=	
		4.5 to 5.5	_	1.0	V	=	
Output voltage	V_{OH}	2.7 to 5.5	V _{cc} -0.2	_	V	$I_{OH} = -100 \mu A$	
		2.7	2.2	_	V	$I_{OH} = -12 \text{ mA}$	
		3.0	2.4	_	V	$I_{OH} = -12 \text{ mA}$	
		3.0	2.0	_	V	$I_{OH} = -24 \text{ mA}$	
		4.5	3.8	_	V	$I_{OH} = -24 \text{ mA}$	
	V _{OL}	2.7 to 5.5	_	0.2	V	I _{OL} = 100 μA	
		2.7	_	0.4	V	I _{OL} = 12 mA	
		3.0	_	0.55	V	I _{OL} = 24 mA	
		4.5	_	0.55	V	I _{OL} = 24 mA	
nput current	I _{IN}	0 to 5.5	_	±5.0	μΑ	V _{IN} = 5.5 V or GND	
Quiescent supply current		5.5	_	20	μΑ	$V_{IN} = V_{CC}$ or GND	
	ΔI_{CC}	3.0 to 3.6	_	500	μΑ	V_{IN} = one input at $(V_{CC}-0.6)V$, other inputs at V_{CC} or GND	

Switching Characteristics

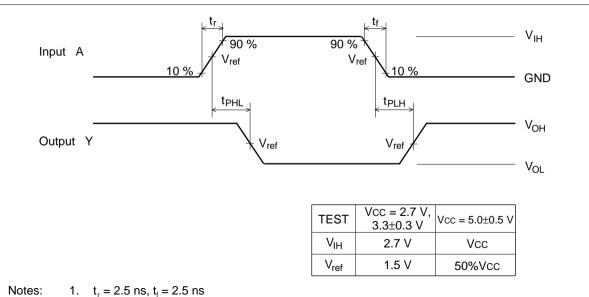
Ta = -40 to 85°C

Item	Symbol	V _{cc} (V)	Min	Тур	Max	Unit	From (Input)	To (Output)
Propagation delay time	t _{PLH}	2.7	_	6.0	9.5	ns	Α	Υ
	$t_{\tiny PHL}$	3.3±0.3	1.5	5.0	8.5	ns	_	
		5.0±0.5	_	3.5	7.0	ns	_	
Input capacitance	C _{IN}	2.7	_	3.0	_	рF		
Output capacitance	Co	2.7	_	15.0	_	рF		

Test Circuit

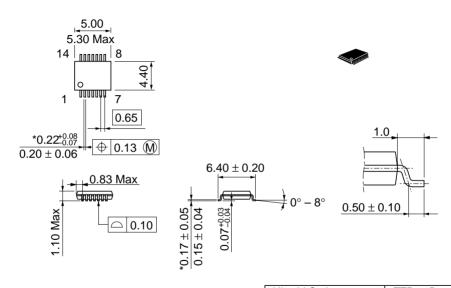


Waveforms

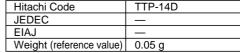


Input waveform: PRR = 10 MHz, duty cycle 50%.

Unit: mm



*Dimension including the plating thickness
Base material dimension



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